

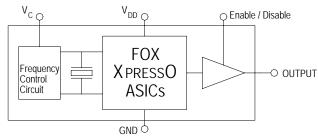
## LVPECL 7 x 5mm 3.3V VCXO

**Model: FVXO-PC73 SERIES** 

Frea: 0.75 MHz to 1.35GHz

#### **Features**

- XTREMELY Low Jitter
- Low Cost
- XPRESS Delivery
- Frequency Resolution to six decimal places
- Absolute Pull Range (APR) of ±50ppm
- -20 to +70°C or -40 to +85°C operating temperatures
- Tri-State Enable / Disable Feature
- Industry Standard Package, Footprint & Pin-Out
- Fully RoHS compliant
- Gold over Nickel Termination Finish
- Serial ID with Comprehensive Traceability



For more information -- Click on the drawing

## **Description**

The Fox XPRESSO Crystal Oscillator is a breakthrough in configurable Frequency Control Solutions. XPRESSO utilizes a family of proprietary ASICs, designed and developed by Fox, with a key focus on noise reduction technologies.

The 3<sup>rd</sup> order Delta Sigma Modulator reduces noise to the levels that are comparable to traditional Bulk Quartz and SAW oscillators. The ASICs family has ability to select the output type, input voltages, and temperature performance features.

With the XPRESS lead-time, low cost, low noise, wide frequency range, excellent ambient performance, XpressO is an excellent choice over the conventional technologies.

Finished XPRESSO parts are 100% final tested.







### **Applications**

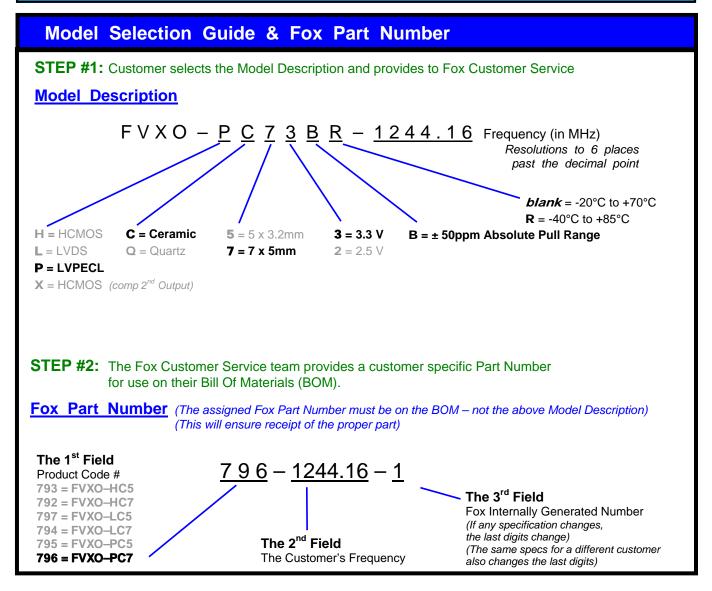
- · ANY application requiring an oscillator
- SONET
- Ethernet
- Storage Area Network
- Broadband Access
- Microprocessors / DSP / FPGA
- Industrial Controllers
- Test and Measurement Equipment
- Fiber Channel

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This example, FVXO-PC73BR-1244.16 = Voltage Controlled, LVPECL Output, Ceramic, 7 x 5mm Package, 3.3V, ±50 PPM Absolute Pull Range, -40 to +85°C Temperature Range, at 1244.16 MHz

Absolute Maximum Ratings (Useful life may be impaired. For user guidelines only, not tested)			
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Input Voltage	$V_{DD}$		-0.5V to +5.0V
Operating Temperature	T <sub>AMAX</sub>		–55°C to +105°C
Storage Temperature	T <sub>STG</sub>		–55°C to +125°C
Junction Temperature			150°C
ESD Sensitivity	HBM	Human Body Model	1 kV





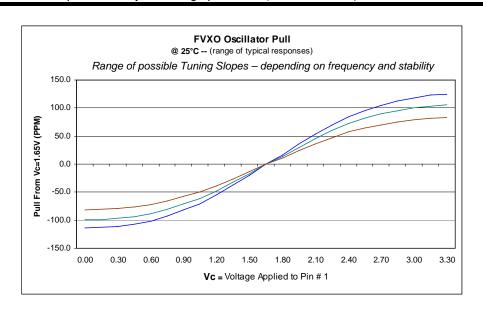
Electrical Characteristics			
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Frequency Range	Fo		0.750 MHz to 1.35 GHz
Absolute Pull Range Note 1	APR		± 50 ppm MIN
Temperature Range	T <sub>O</sub>	Standard operating Optional operating Storage	-20°C to +70°C -40°C to +85°C -55°C to +125°C
Supply Voltage	$V_{DD}$	Standard	3.3 V ± 5%
Input Current (@ Standard Load)	I <sub>DD</sub>	Standard Load	120 mA
Output Load		Standard	50 Ohms into $V_{DD}$ -2 $V_{DC}$ . TYP.
Start-Up Time	Ts		10 mS
Output Enable / Disable Time			100 nS
Moisture Sensitivity Level	MSL	JEDEC J-STD-20	1
Termination Finish			Au

Note 1 – Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock and vibration.

Frequency Control (V <sub>c</sub> ) Input pin # 1			
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Control Voltage Tuning Slope <sup>1</sup>		0V to V <sub>DD</sub>	40 ~ 75 ppm/V Typ <sup>2</sup>
Control Voltage Linearity <sup>2</sup>	L <sub>VC</sub>		± 10%
Control Voltage Tuning Range	V <sub>C</sub>		0V ~ 3.3V
Modulation Bandwidth	BW		10 kHz
Nominal Control Voltage	$V_{CNOM}$		1.65V

#### NOTES:

- Actual slope is affected by frequency and accuracy settings.
- For an example of linearity, see the graph below. (The middle line represents the default Fox factory setting)

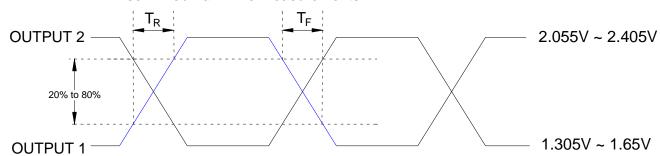






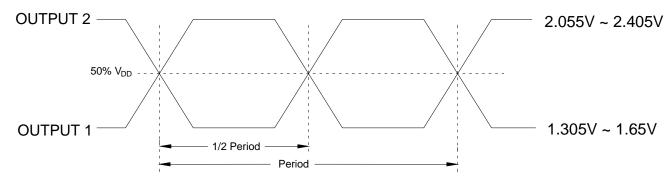
Output Wave Characteristics			
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)
Low Output Voltage	V <sub>OL</sub>	0.75 MHz to 1.35 GHz	1.305V ~ 1.65V
High Output Voltage	V <sub>OH</sub>	0.75 MHz to 1.35 GHz	2.055V ~ 2.405V
Typical Complementary Difference	$V_{P-P}$	0.75 MHz to 1.35 GHz	0.750 V <sub>Р-Р</sub> Тур
Output Symmetry (See Drawing Below)		@ 50% V <sub>P-P</sub> Level	45% ~ 55%
Output Enable (PIN # 2) Voltage	V <sub>IH</sub>		> 70% V <sub>DD</sub>
Output Disable (PIN # 2) Voltage	V <sub>IL</sub>		$< 30\% V_{DD}$
Cycle Rise Time (See Drawing Below)	T <sub>R</sub>	0.75 MHz to 1.35 GHz	400 pS <sub>(20%~80%)</sub>
Cycle Fall Time (See Drawing Below)	T <sub>F</sub>	0.75 MHz to 1.35 GHz	400 pS <sub>(80%~20%)</sub>

#### Rise Time / Fall Time Measurements



#### **Oscillator Symmetry**

Ideally, Symmetry should be 50/50 for 1/2 period -- Other expressions are 45/55 or 55/45





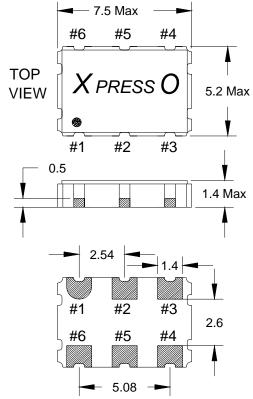
**Actual part marking** 

is depicted.

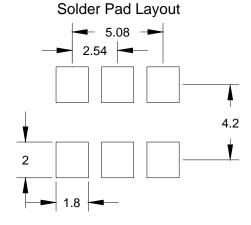
See **Traceability** (pg. 9) for more information



## **Mechanical Dimensional Drawing & Pad Layout**



## Recommended



Note: XPRESSO LVDS XOs are designed to fit on Industry standard, 6 pad, layouts.

#### **Pin Connections**

#1) V<sub>c</sub> #4) Output

#2) E/D #5) Output 2

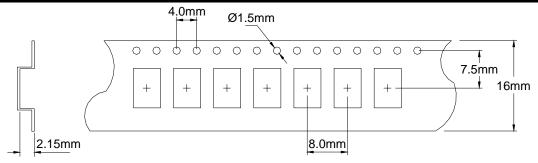
#3) GND #6) V<sub>DD</sub>

Drawing is for reference to critical specifications defined by size measurements. Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary

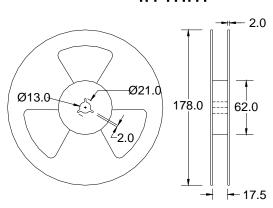




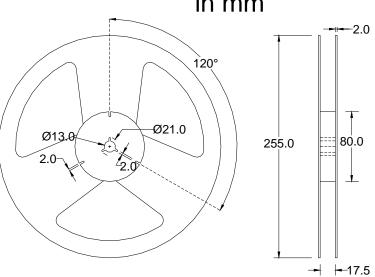
## **Tape and Reel Dimensions**



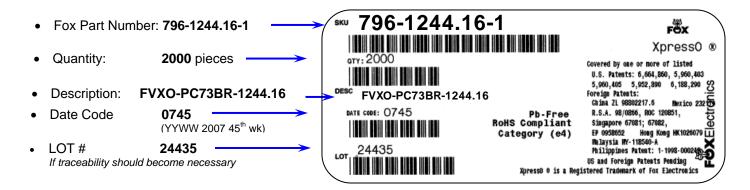
# 1k Reel Dimensions in mm



# 2k Reel Dimensions in mm



## Labeling (Reels and smaller packaging are labeled with the below)



An additional identification code is contained internally if tracking should ever be necessary

